

FIG. 1

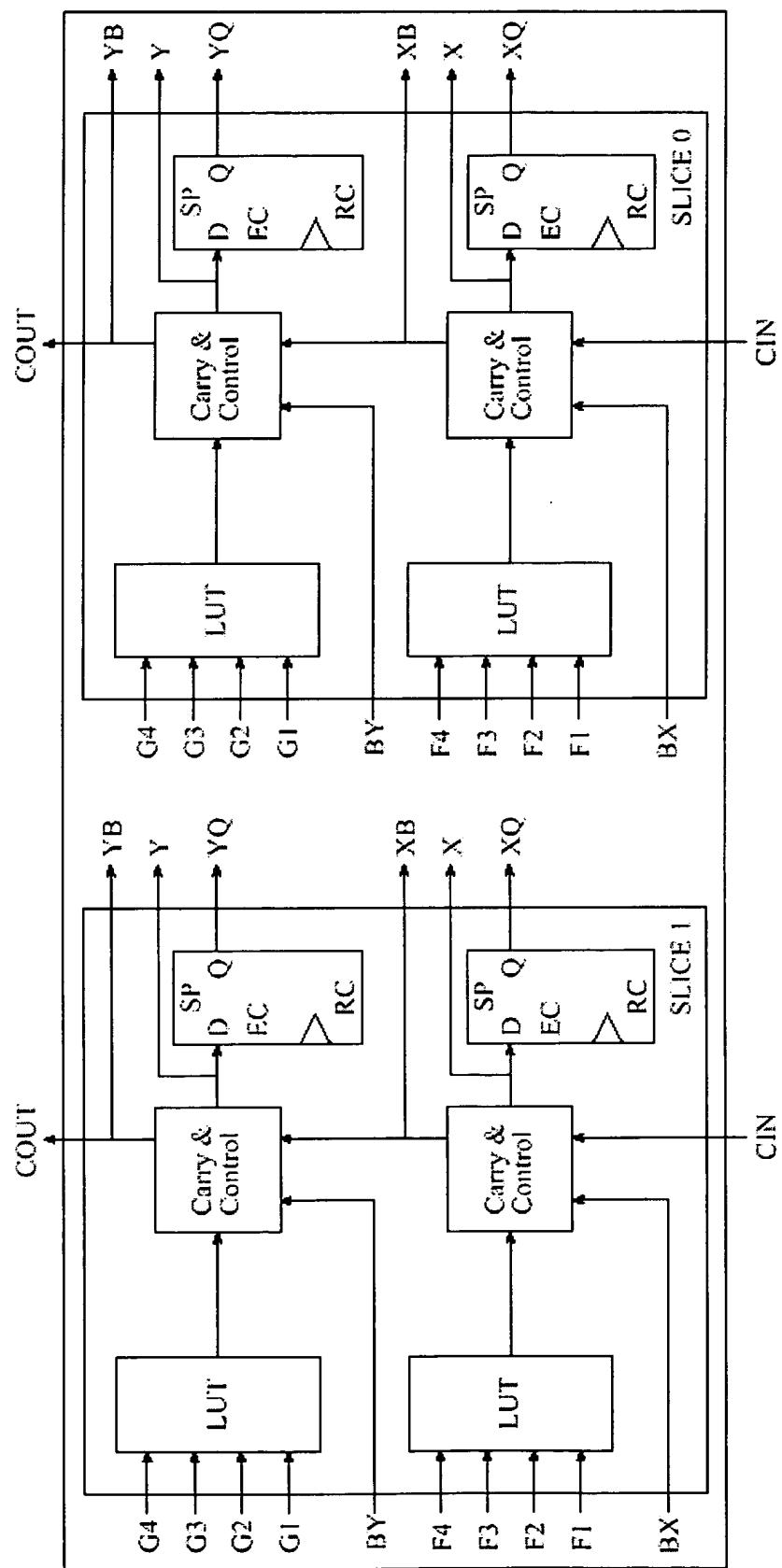
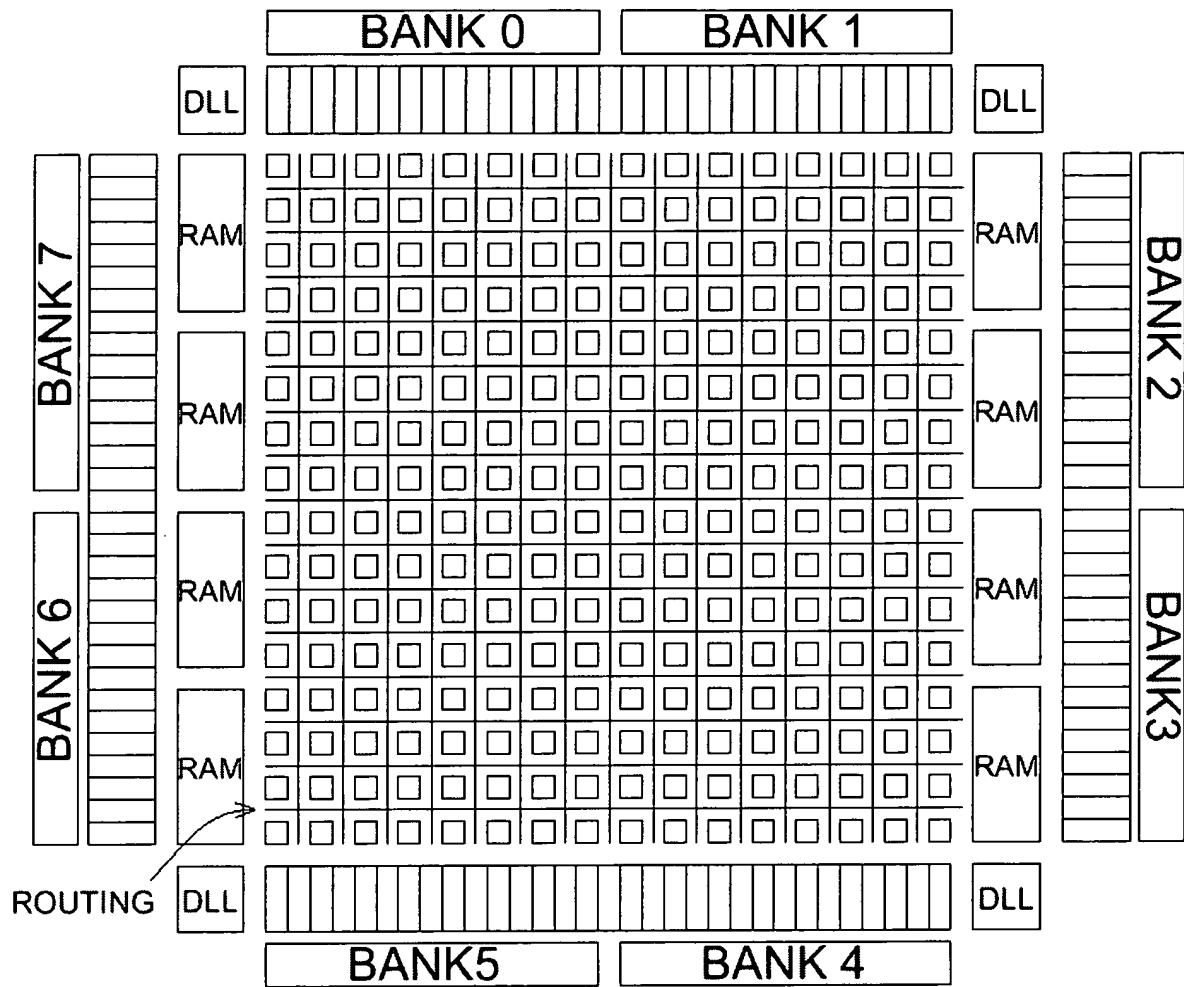


FIG. 2



DLL = DELAY LOCKED LOOP

RAM = BLOCK RAM - VARIOUS FORM FACTORS FROM  
4096x1 TO 256x16

= 2 SLICE CLB       = I/O

**BANK N** = MULTI-STANDARD I/O SUPPORT

FIG. 3

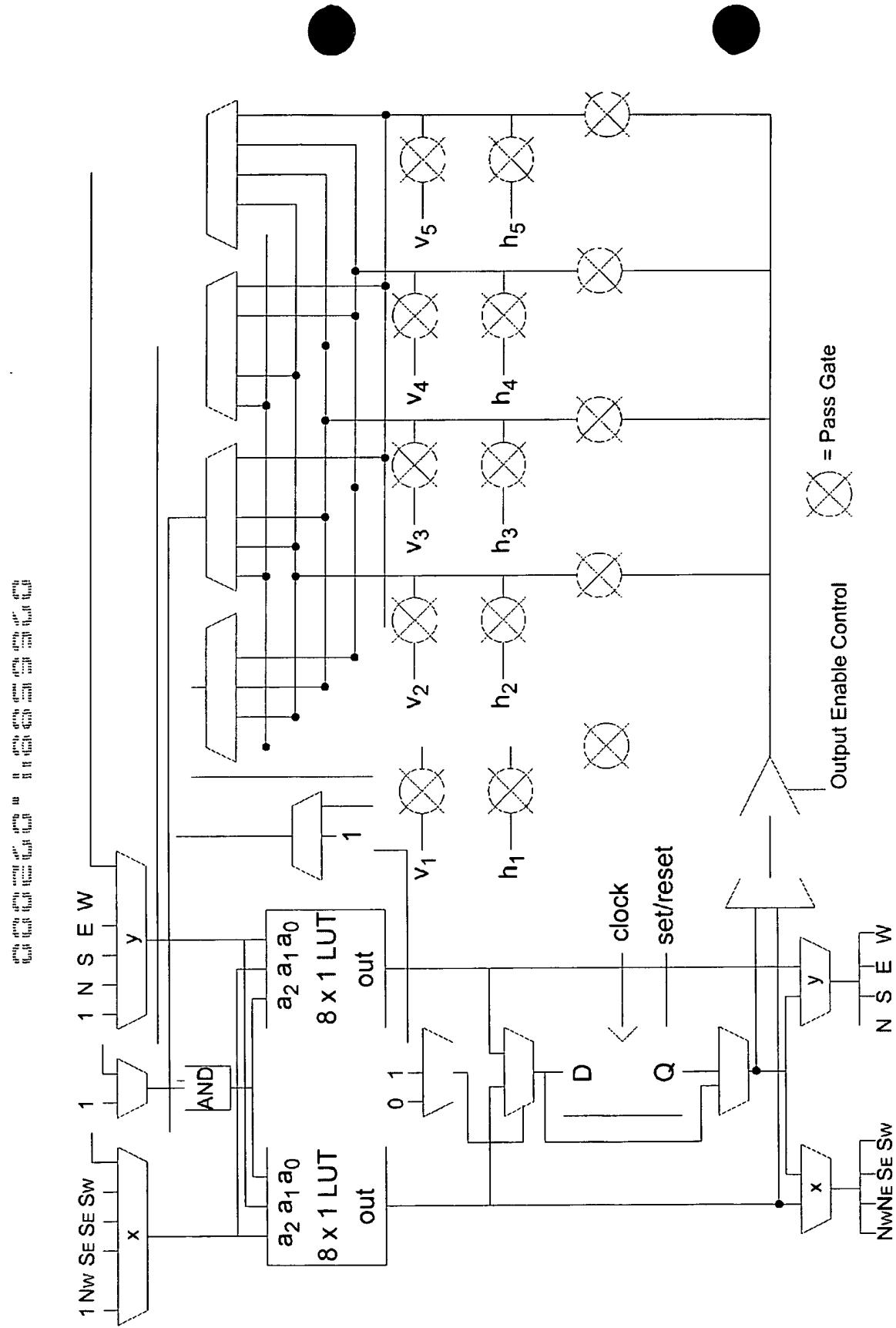


FIG. 4

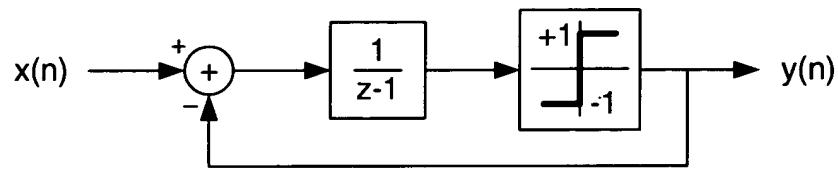


FIG. 5

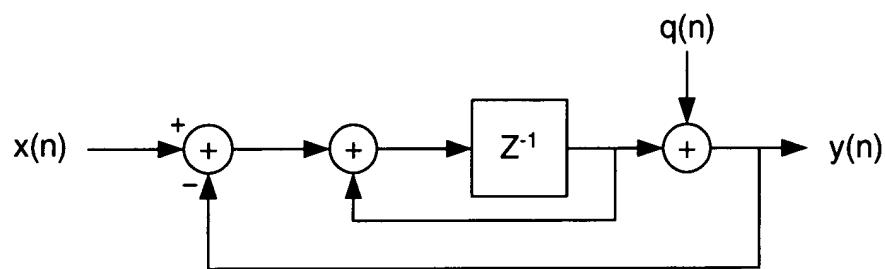


FIG. 6

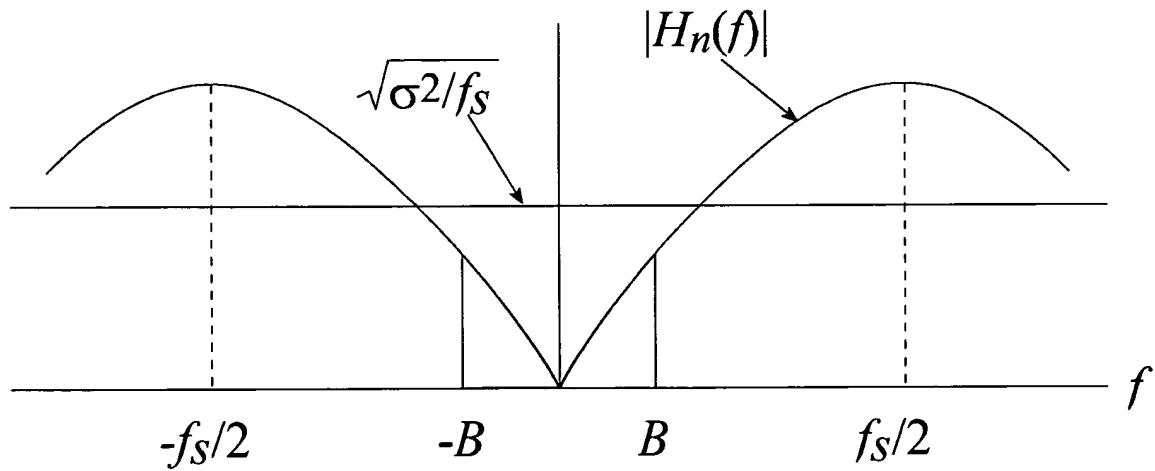


FIG. 7

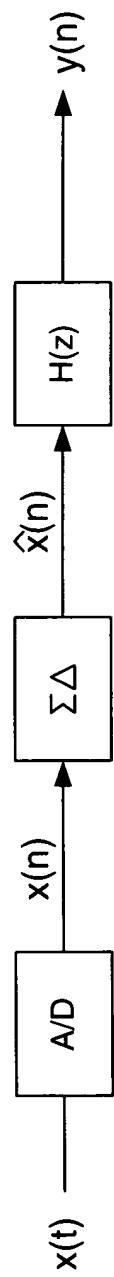


FIG. 8

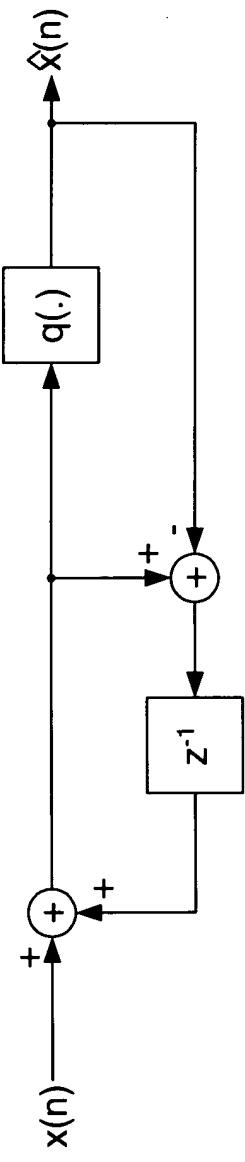


FIG. 9

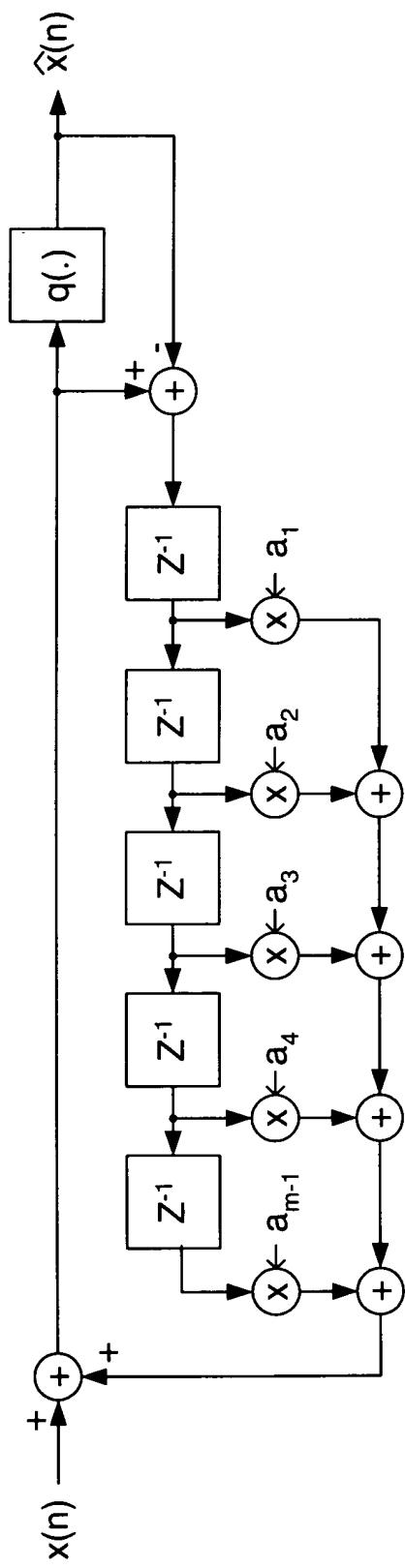


FIG. 10

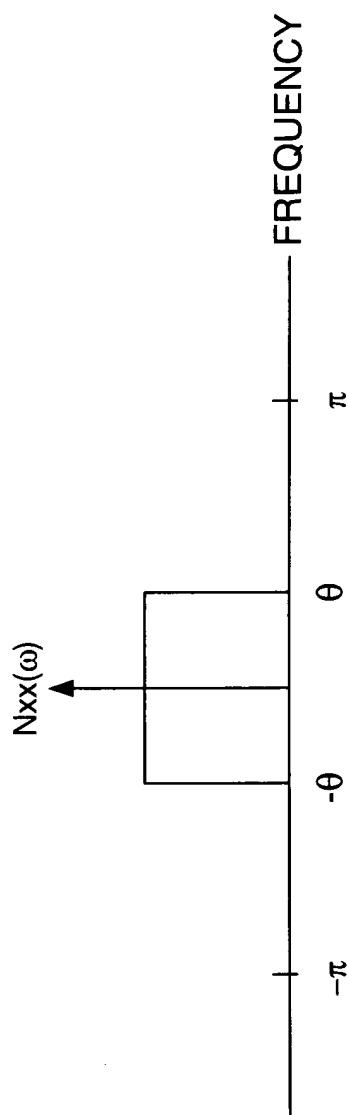
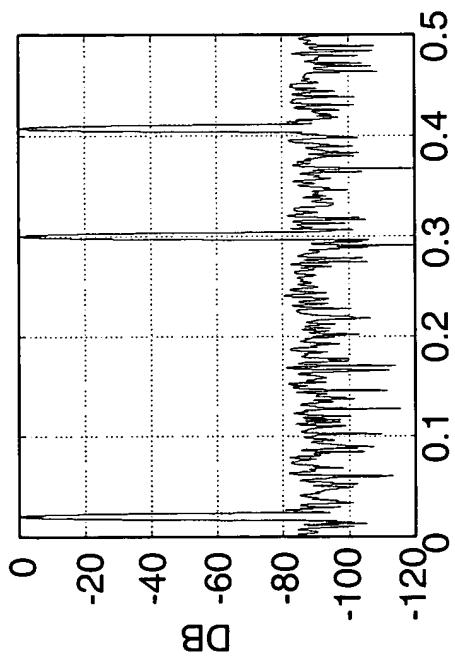
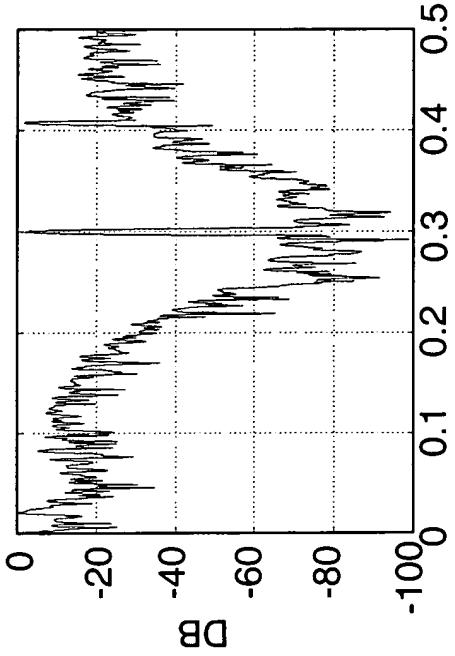


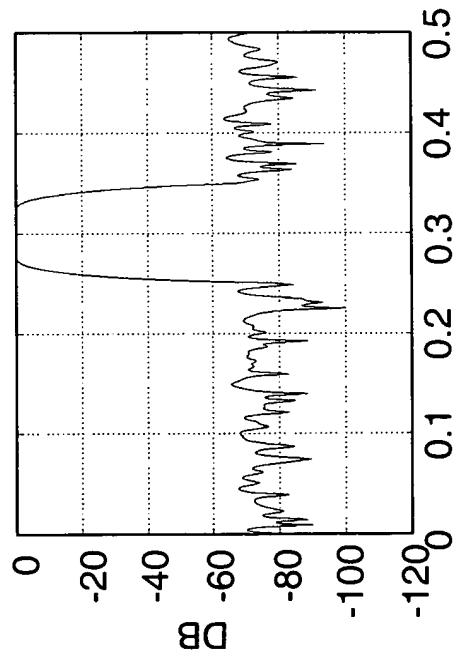
FIG. 12



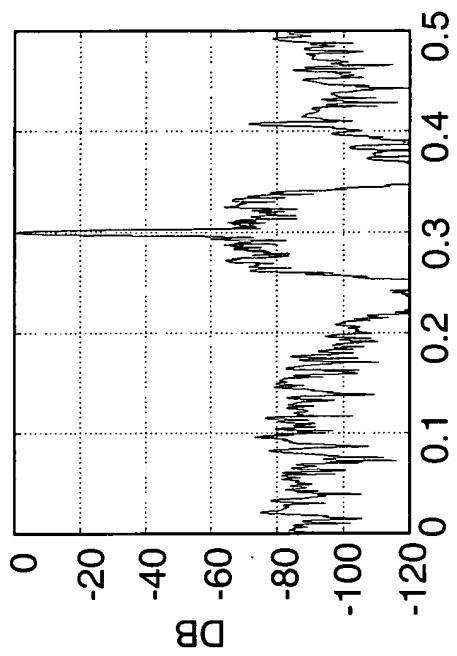
**FIG. 11A**



**FIG. 11B**



**FIG. 11C**



**FIG. 11D**

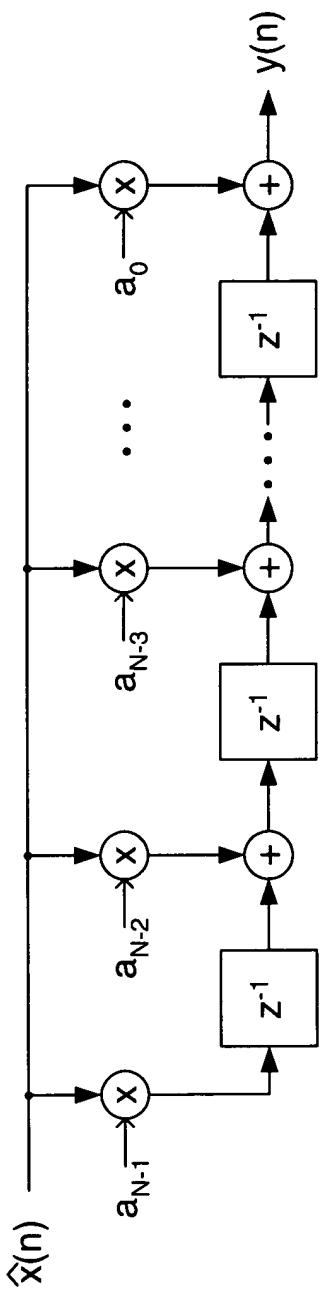


FIG. 13

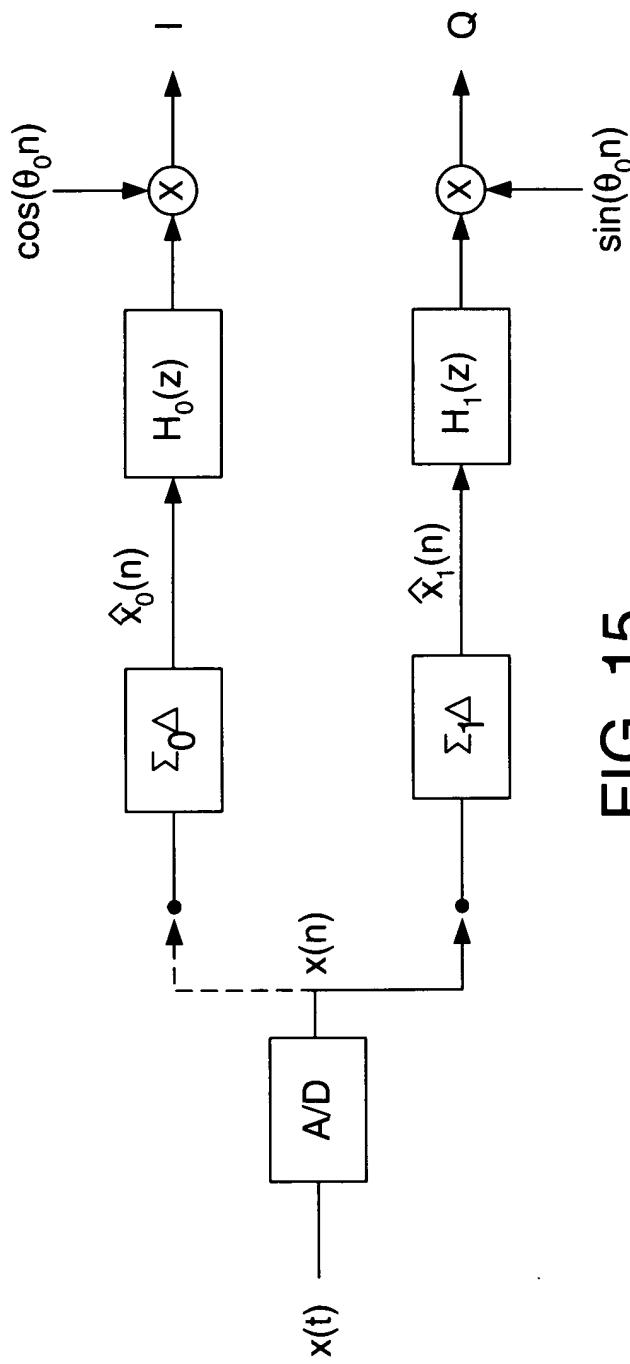


FIG. 15

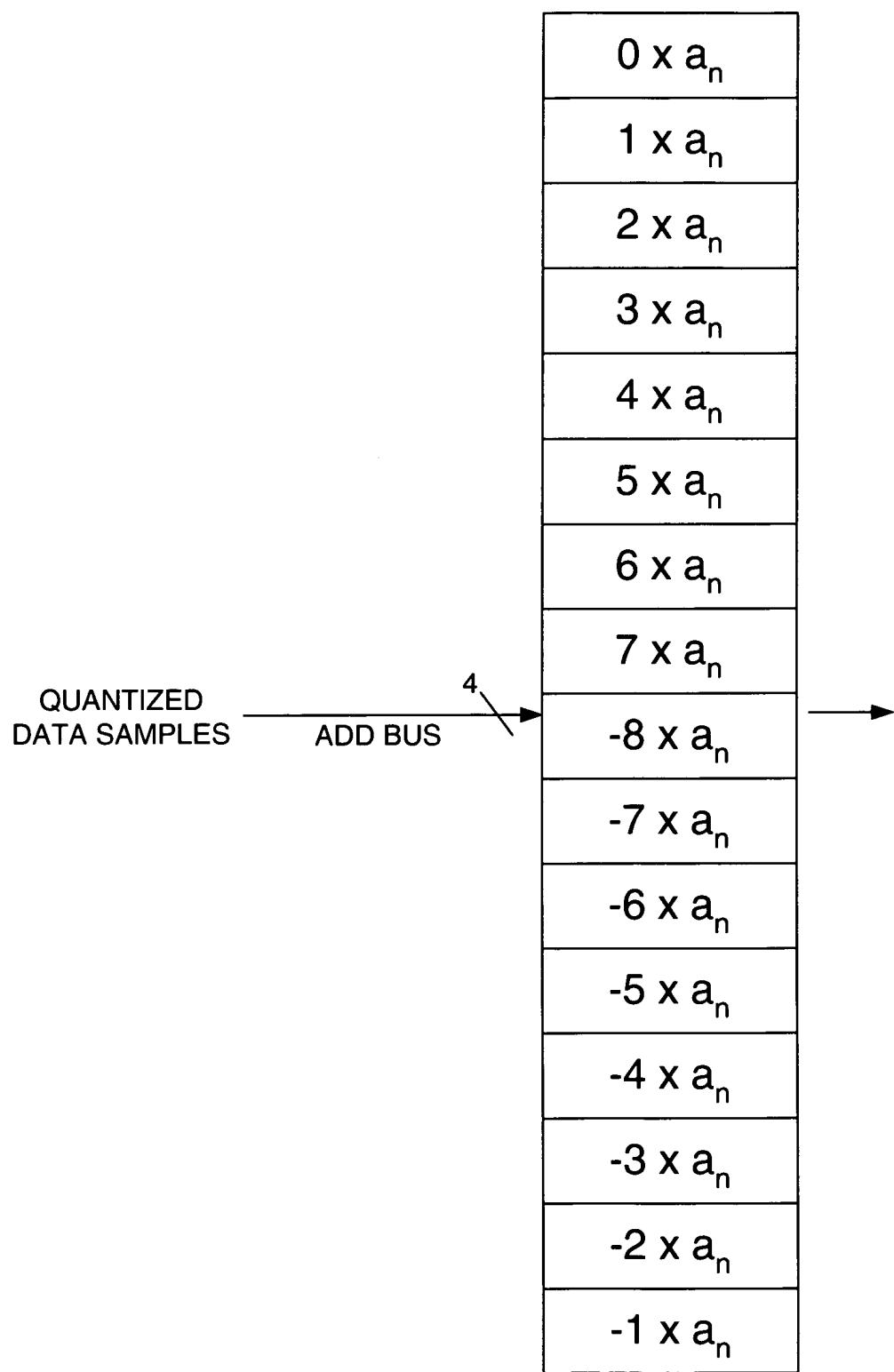
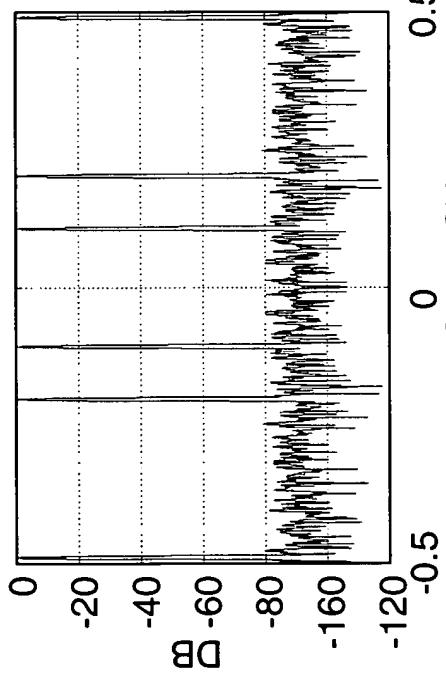
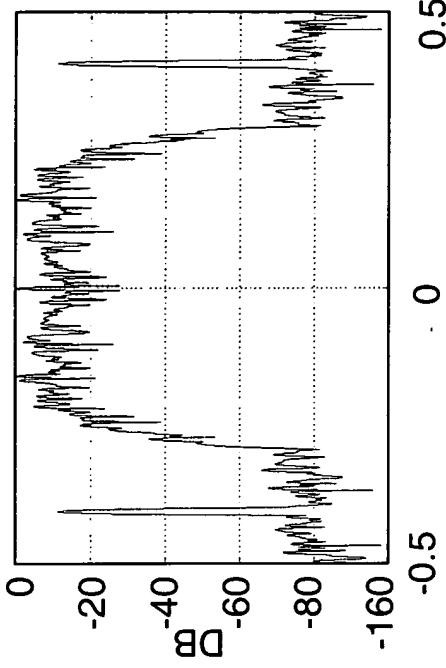


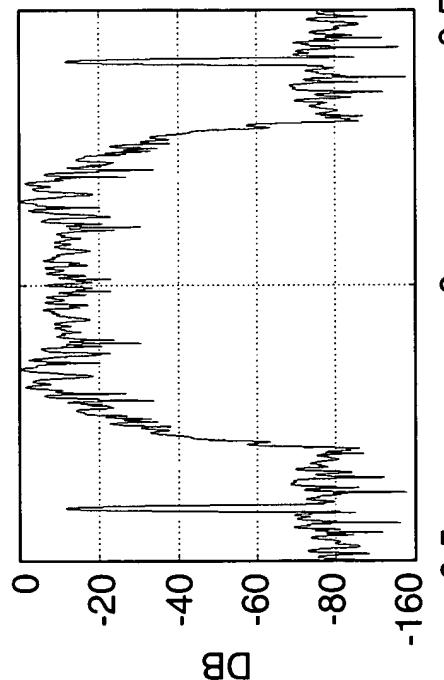
FIG. 14



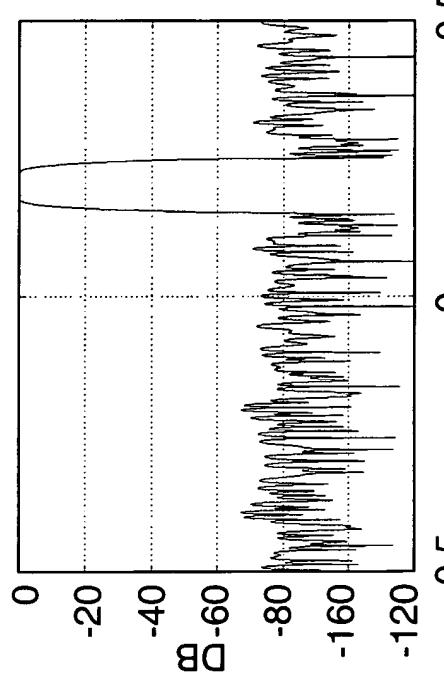
**FIG. 16A**



**FIG. 16B**



**FIG. 16C**



**FIG. 16D**

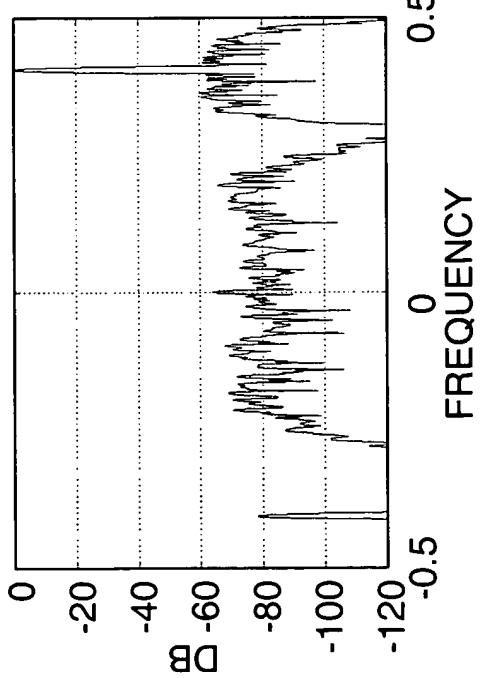


FIG. 16E

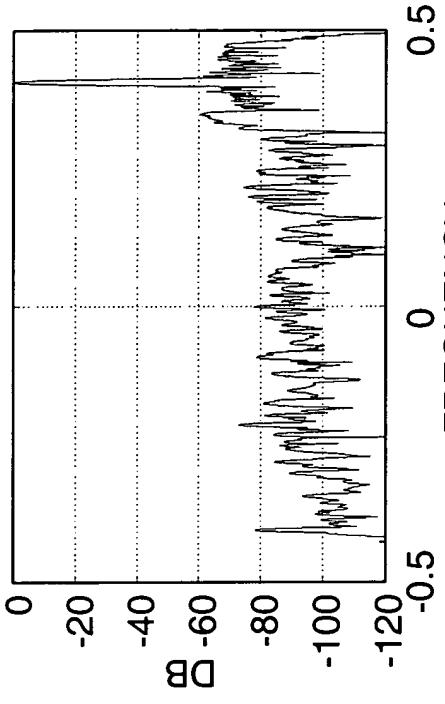


FIG. 16F

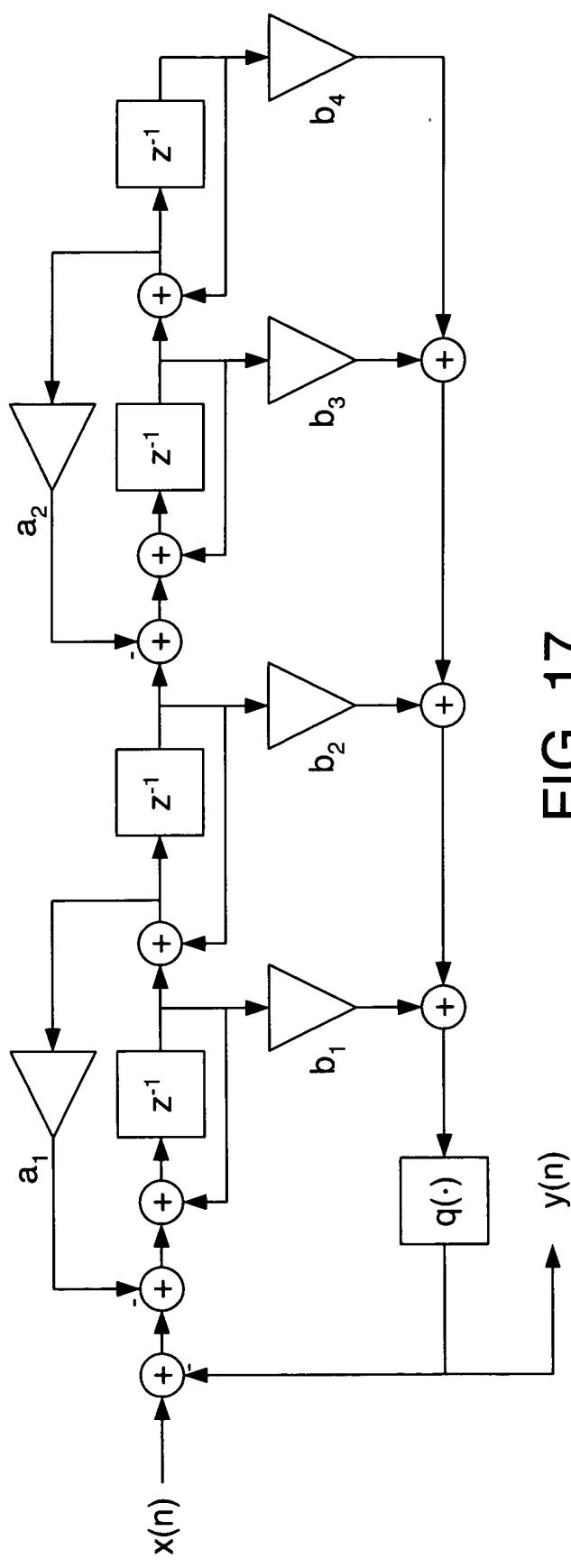


FIG. 17

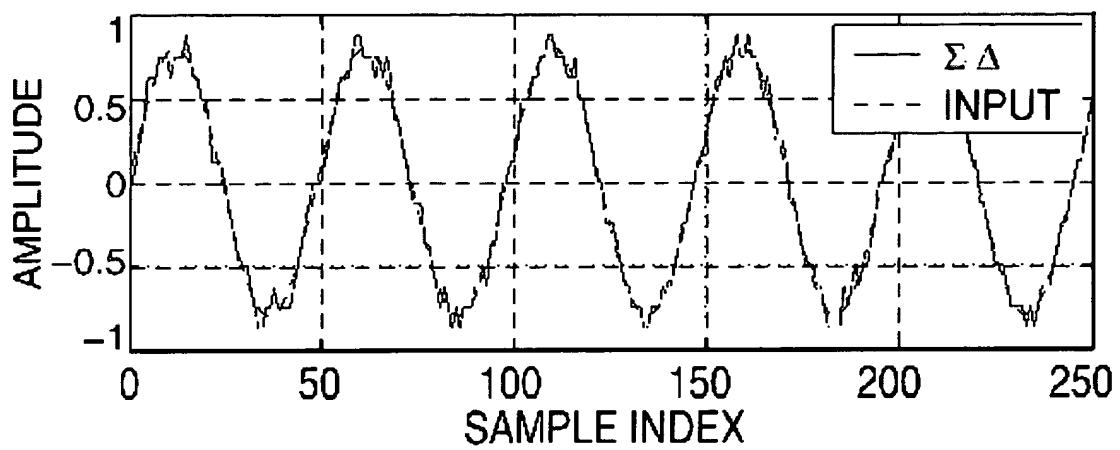


FIG. 18A

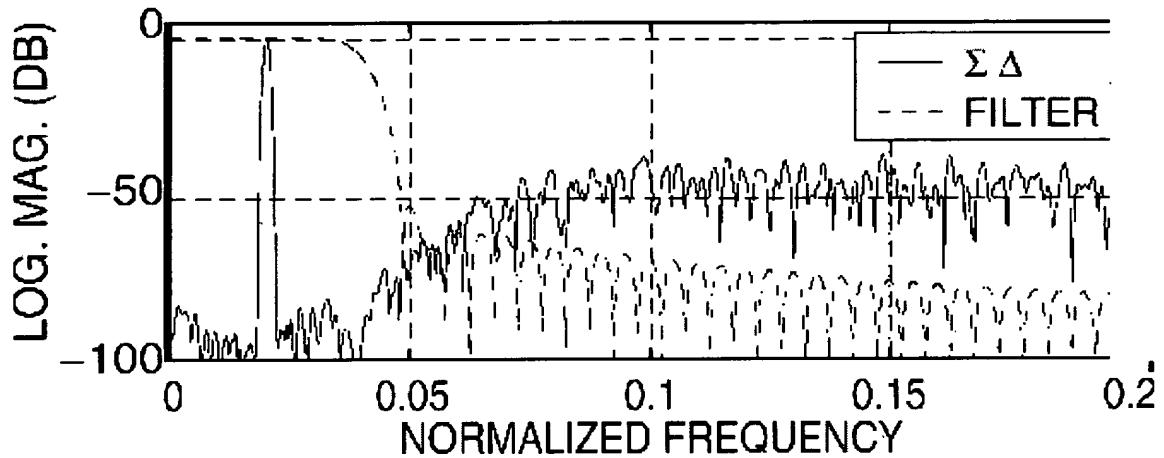


FIG. 18B

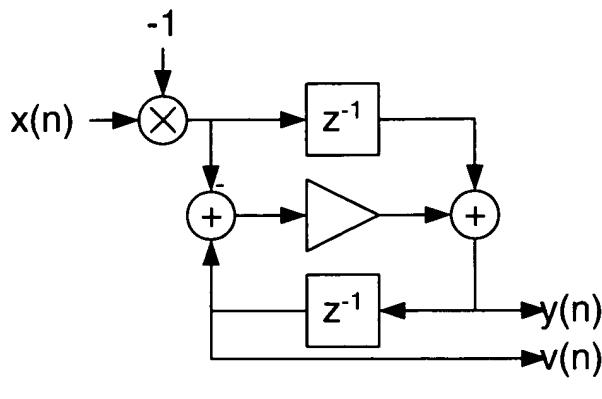


FIG. 19A

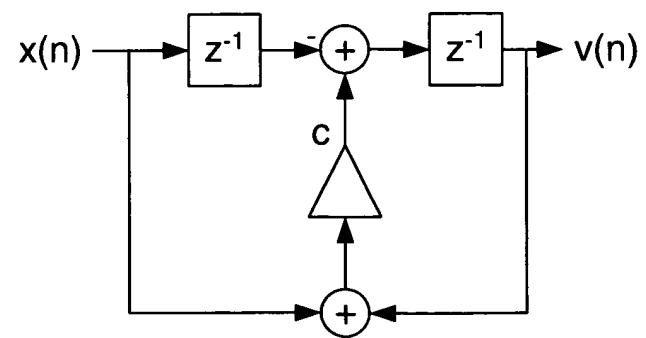


FIG. 19B

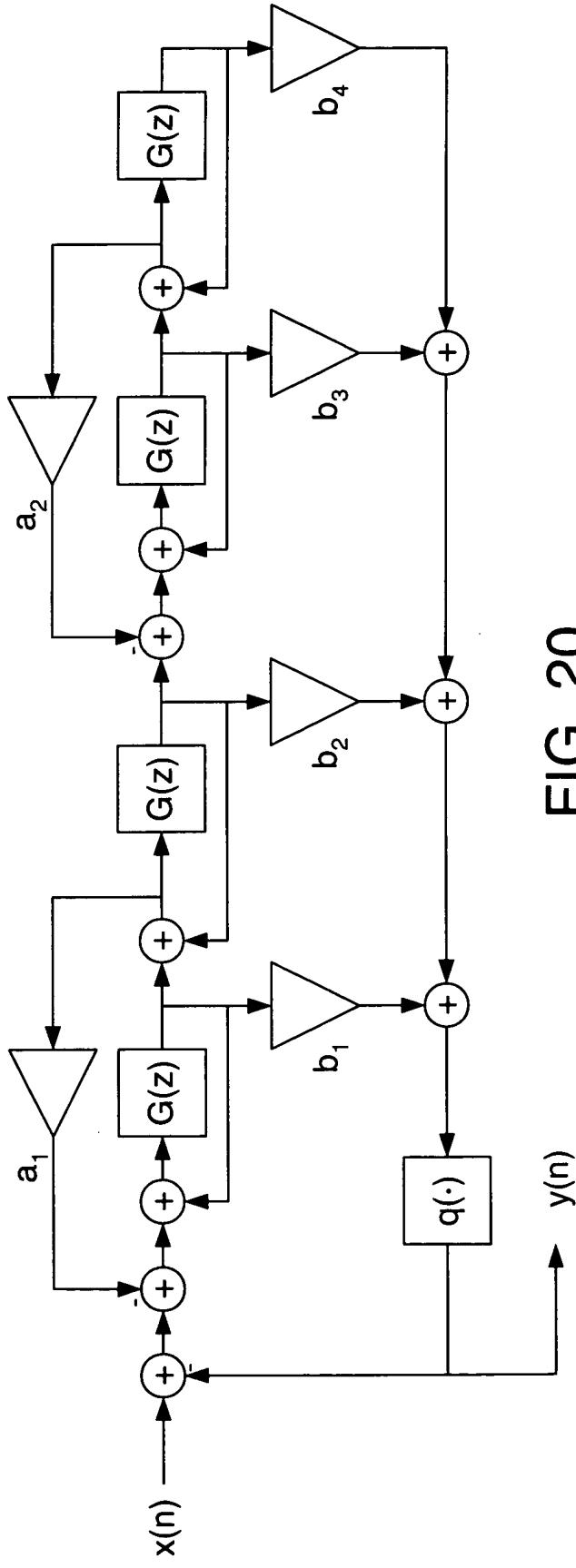


FIG. 20

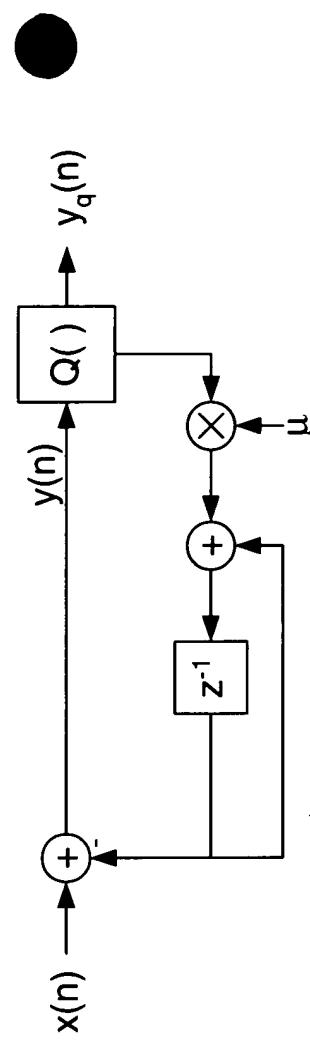


FIG. 24

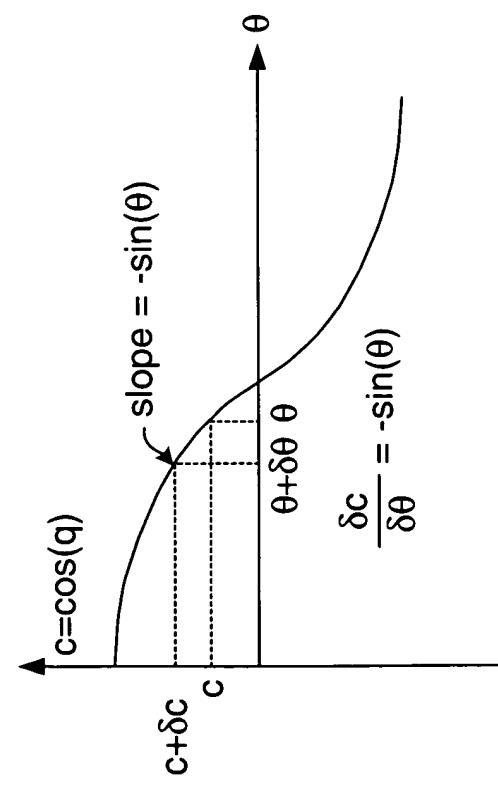


FIG. 22

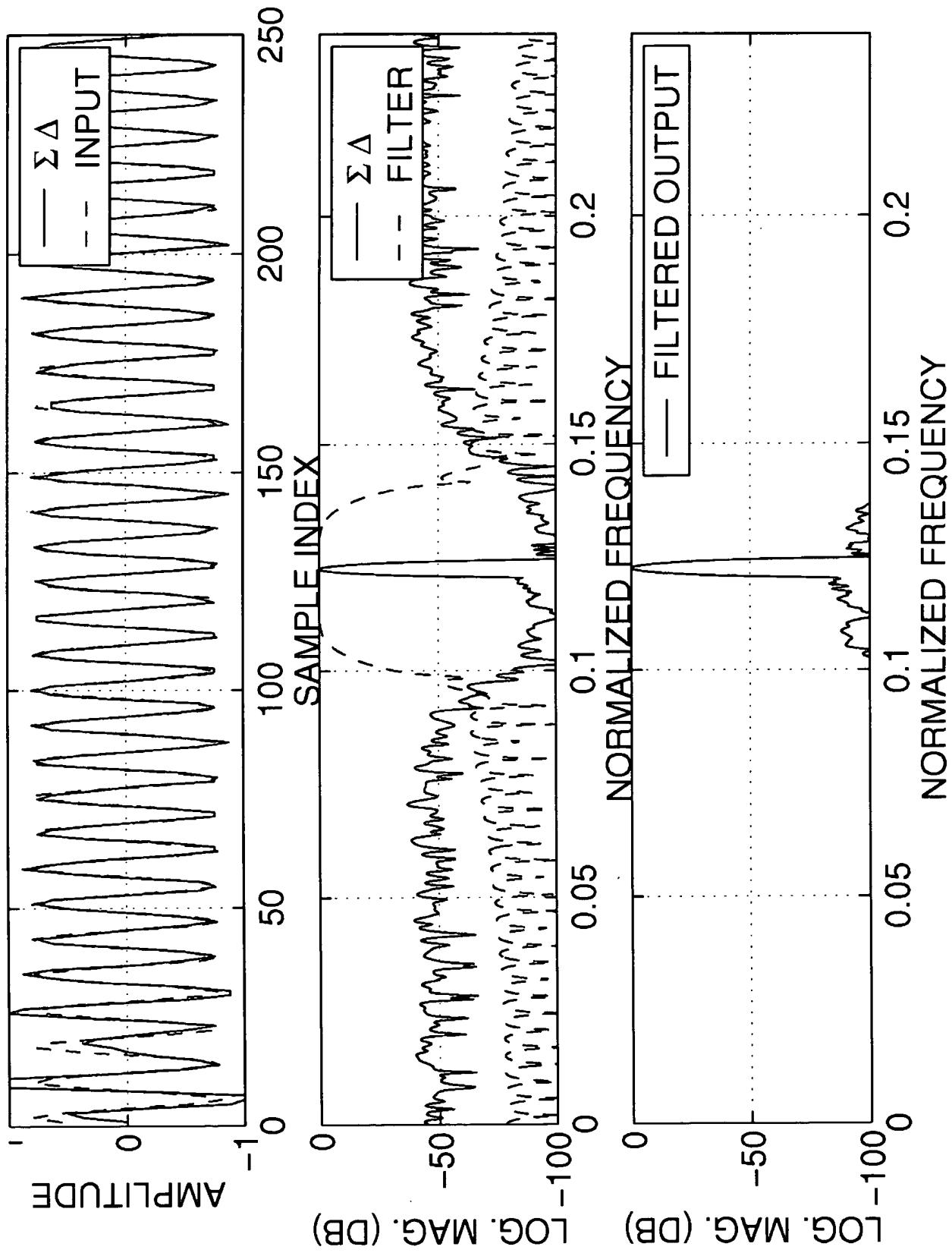


FIG. 21

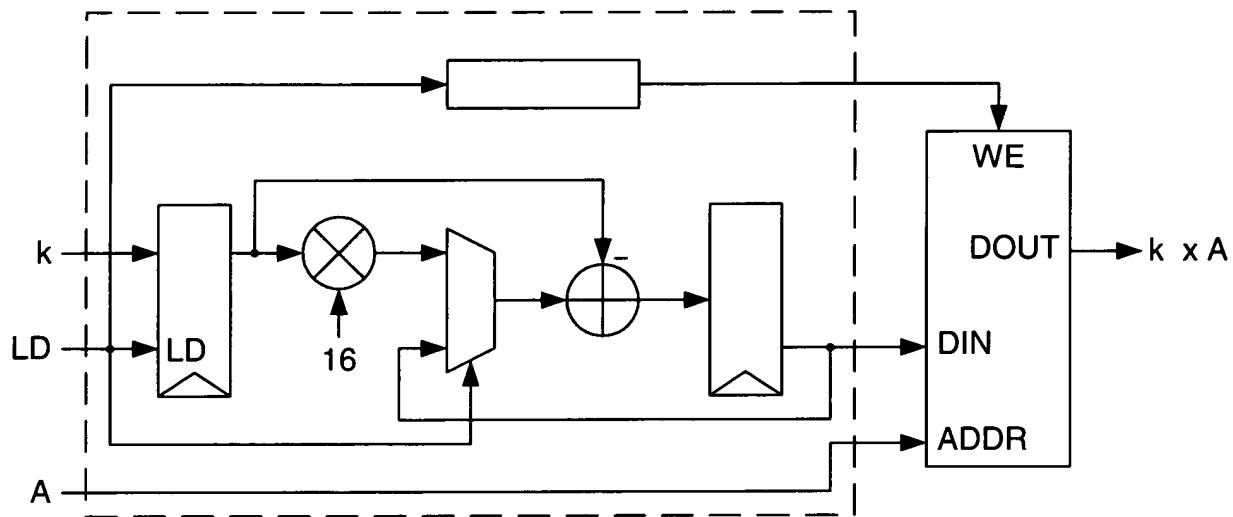


FIG. 23

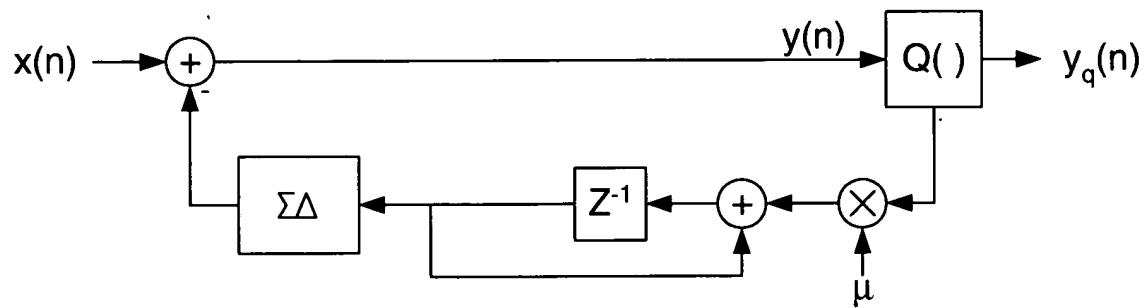
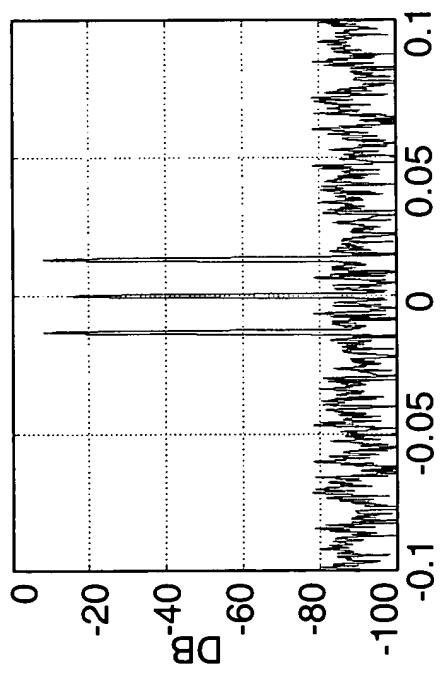
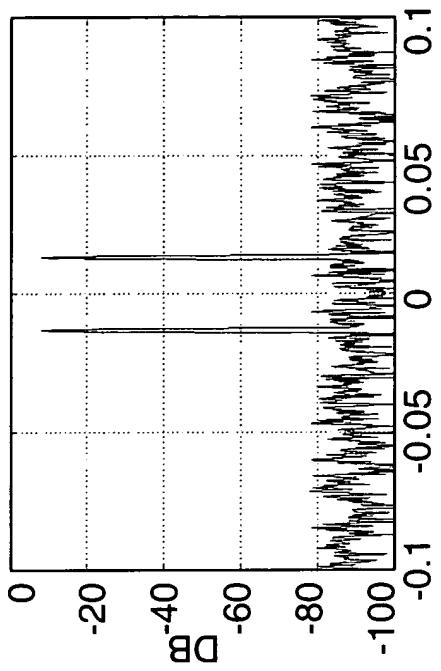


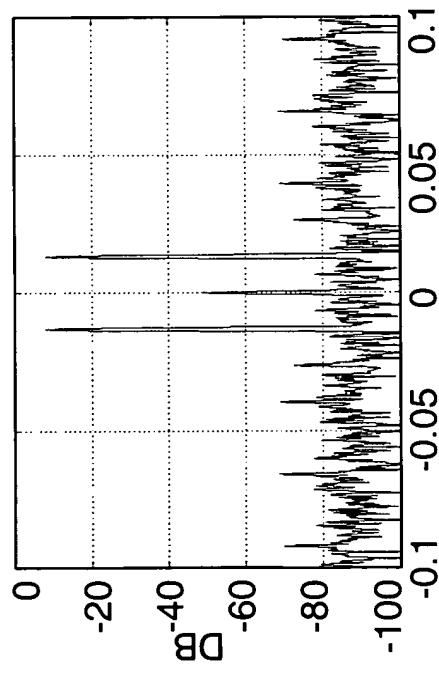
FIG. 26



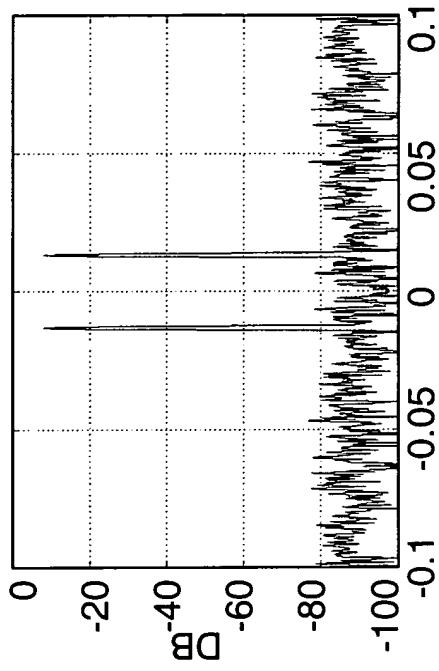
**FIG. 25A**



**FIG. 25B**



**FIG. 25C**



**FIG. 25D**

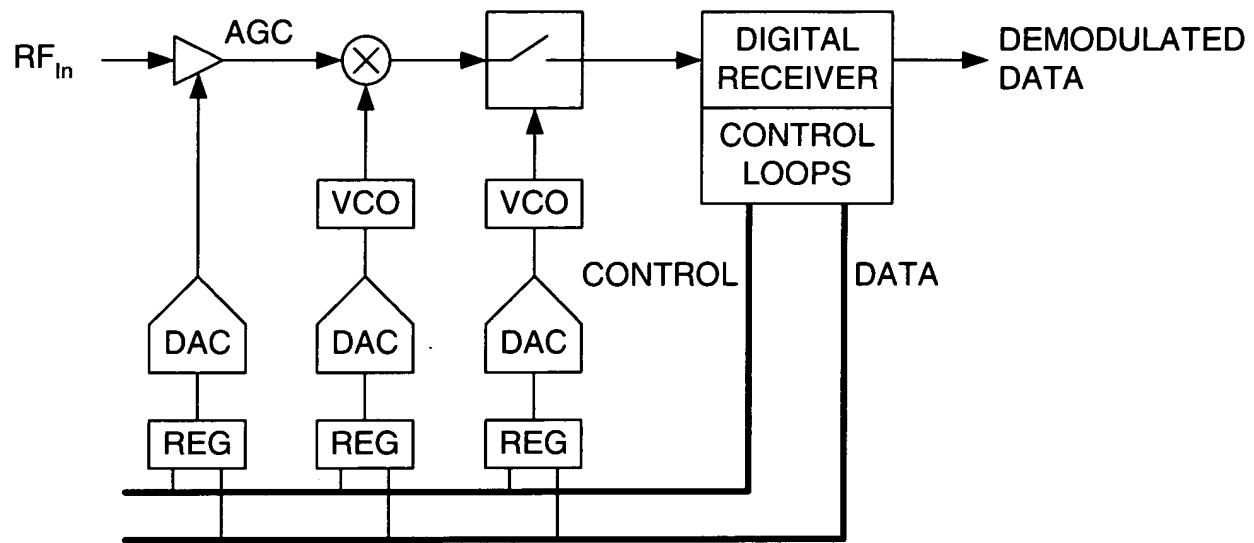


FIG. 27

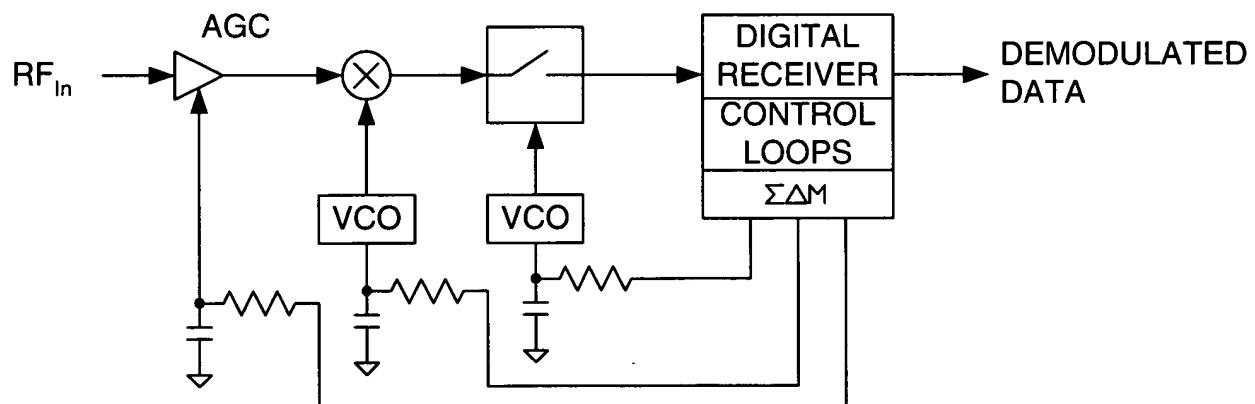


FIG. 28

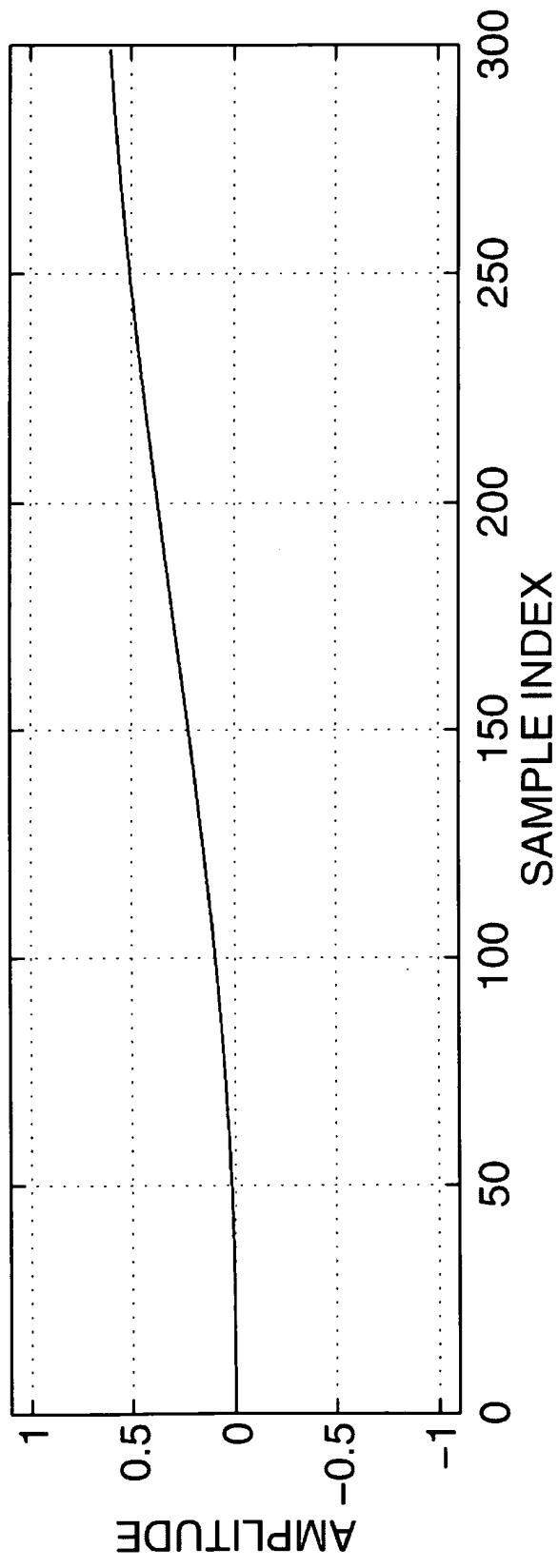
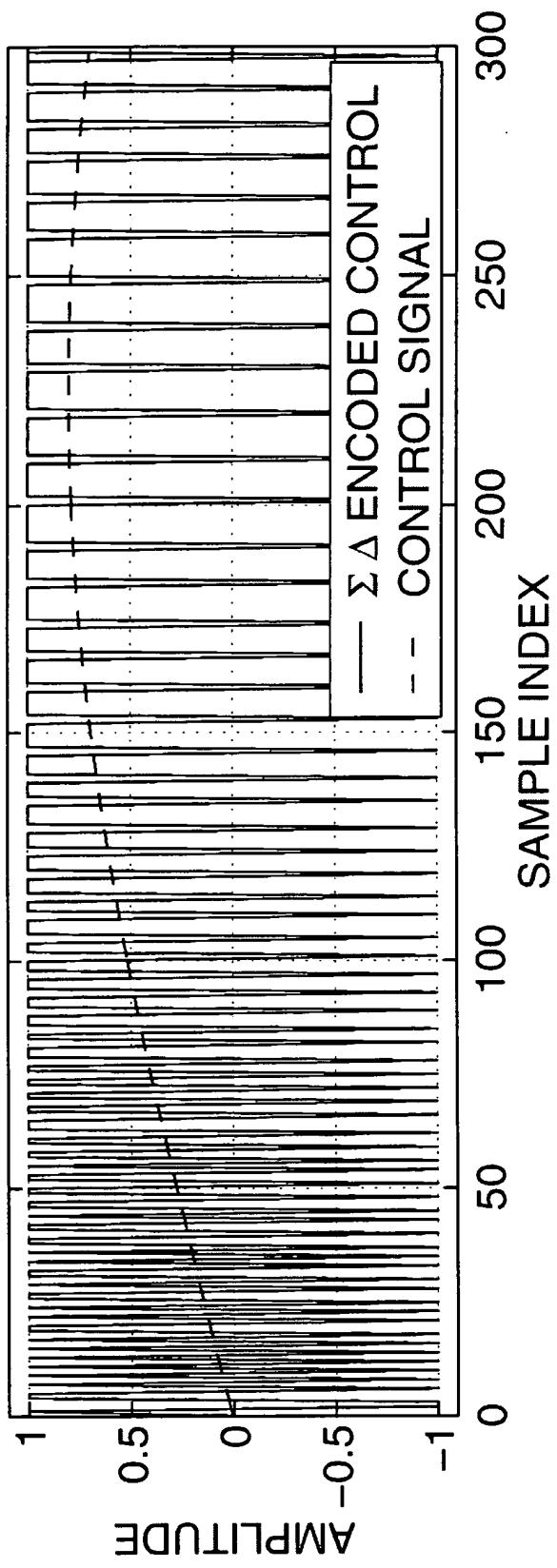


FIG. 29

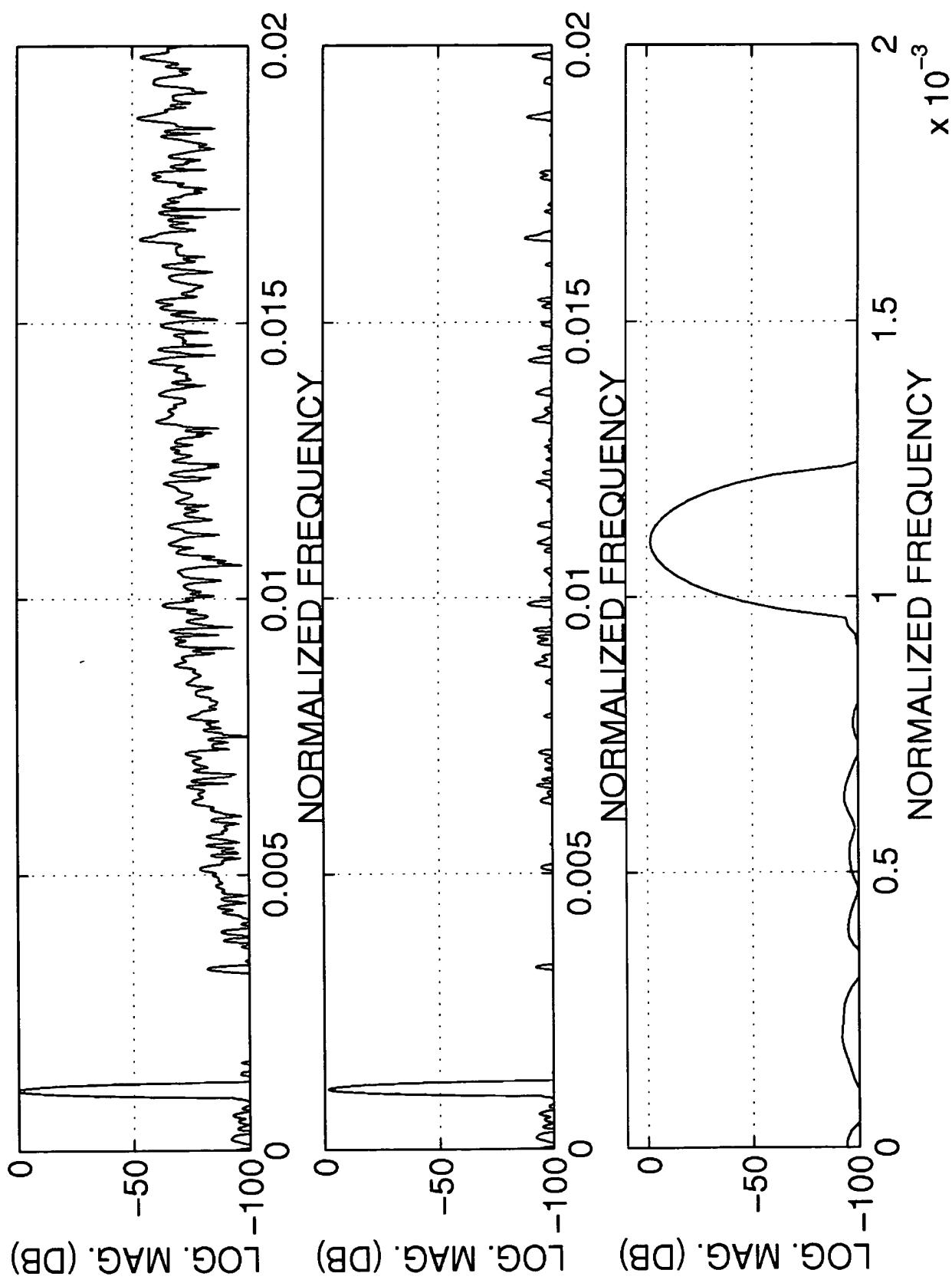


FIG. 30